

Architecture d'un ordinateur: portes logiques (synthèse):

1. Le transistor:



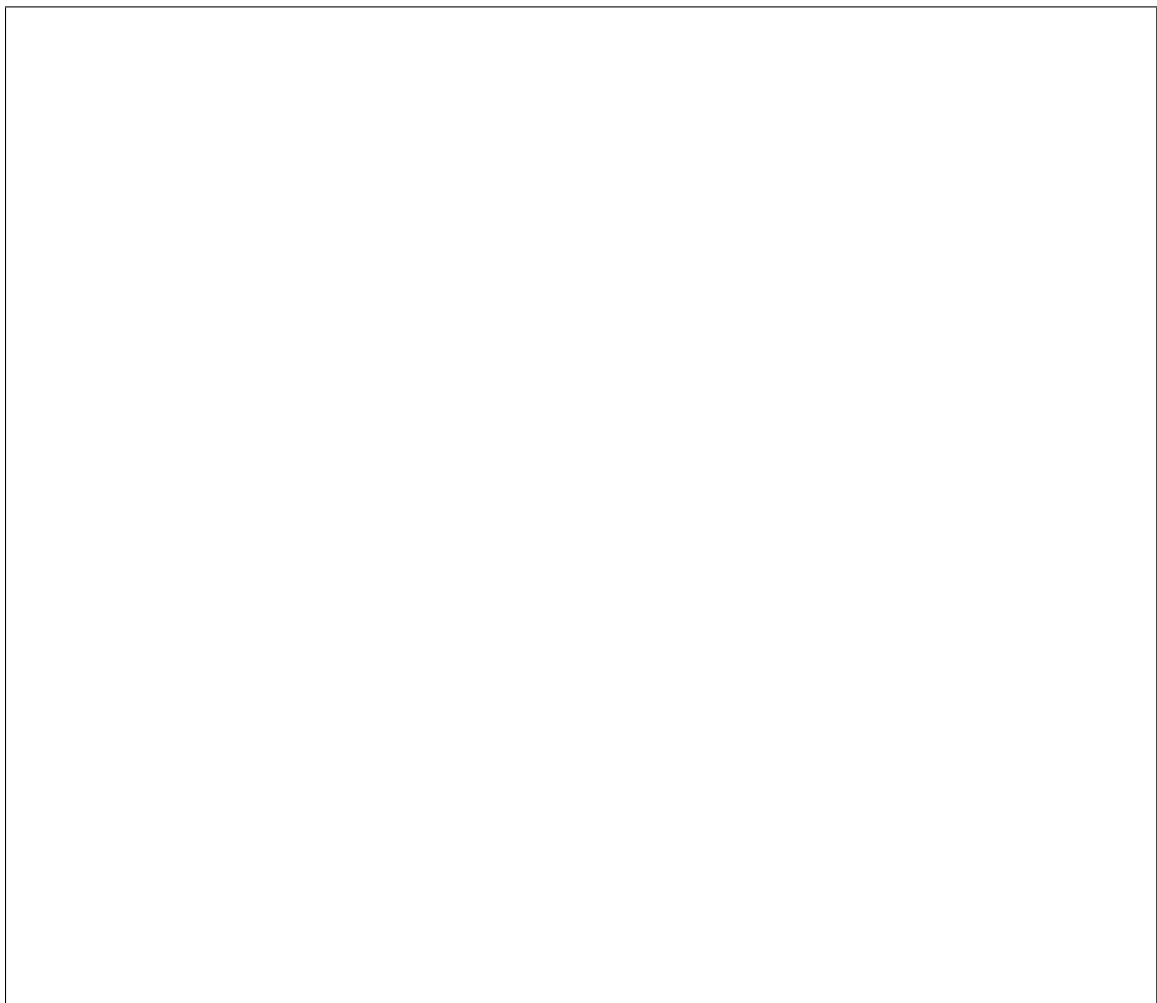
un transistor



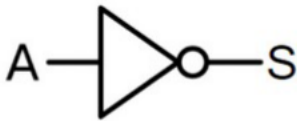

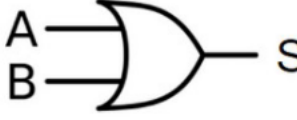

un tube électronique




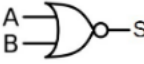
un circuit intégré

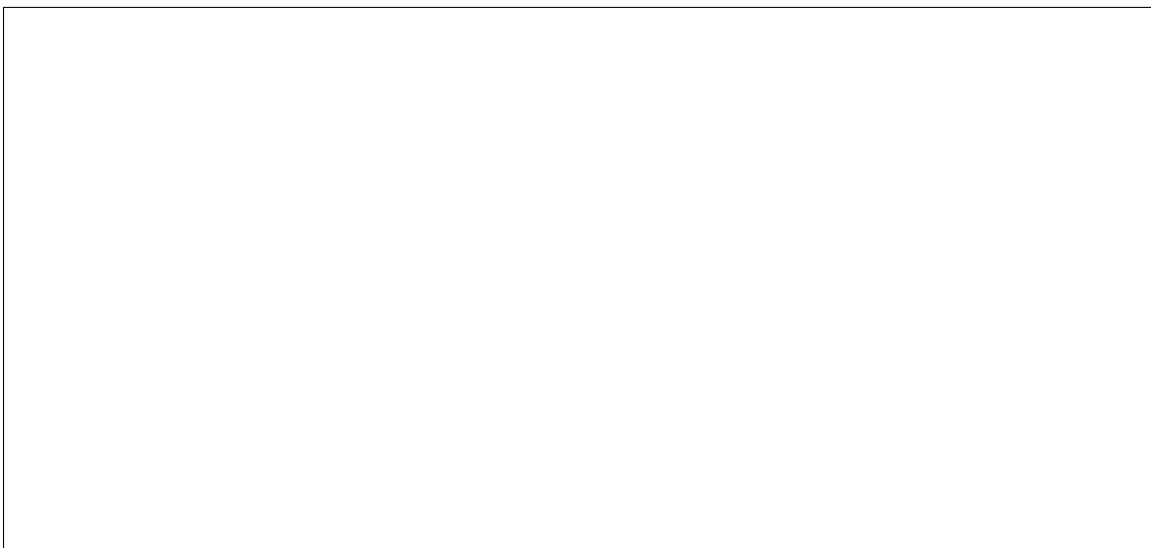


2. Les portes logiques:

Nom	Symbole (ANSI)	Table de vérité
NON		
ET AND		
OU OR		
OU exclusif XOR		

Voici les tables de vérité des portes NAND (Not AND) et NOR (Not OR). :

	A	B	S		A	B	S
	1	1	0		1	1	0
	0	1	1		0	1	0
	1	0	1		1	0	0
	0	0	1		0	0	1



3. pour aller plus loin:

